## Preliminary Data Sheet

## Hybrid Power Module <br> Integrated Power Stage for 460 VAC Motor Drives

These modules integrate a 3-phase inverter in a single convenient package. They are designed for 1.0, 2.0 and 3.0 hp motor drive applications. The inverter incorporates advanced insulated gate bipolar transistors (IGBT) matched with free-wheeling diodes to give optimum performance. The top connector pins are designed for easy interfacing to the user's control board.

- Short Circuit Rated $10 \mu \mathrm{~s}$ @ $125^{\circ} \mathrm{C}$
- Pin-to-Baseplate Isolation Exceeds 2500 Vac (rms)
- Compact Package Outline
- Access to Positive and Negative DC Bus
- UL Recognized

MAXIMUM DEVICE RATINGS ( $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ unless otherwise noted)

| Rating |  | Symbol | Value | Unit |
| :---: | :---: | :---: | :---: | :---: |
| IGBT Reverse Voltage |  | $\mathrm{V}_{\text {CES }}$ | 1200 | V |
| Gate-Emitter Voltage |  | $V_{\text {GES }}$ | $\pm 20$ | V |
| Continuous IGBT Collector Current | 5A120 10A120 <br> 15A120 | ${ }^{\prime} \mathrm{Cmax}$ | $\begin{aligned} & 5.0 \\ & 10 \\ & 15 \end{aligned}$ | A |
| Peak Repetitive IGBT Collector Current (1) | 5A120 10A120 15A120 | ${ }^{\text {I C }}$ (pk) | $\begin{aligned} & 10 \\ & 20 \\ & 30 \end{aligned}$ | A |
| Continuous Diode Current | 5A120 10A120 15A120 | ${ }^{\prime}$ Fmax | $\begin{aligned} & \hline 5.0 \\ & 10 \\ & 15 \end{aligned}$ | A |
| Peak Repetitive Diode Current (1) | 5A120 10A120 15A120 | ${ }^{\mathrm{I}}$ (pk) | $\begin{aligned} & 10 \\ & 20 \\ & 30 \end{aligned}$ | A |
| IGBT Power Dissipation per die ( $\left.\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}\right)$ | 5A120 10A120 15A120 | $\mathrm{PD}_{\mathrm{D}}$ | $\begin{aligned} & \hline 43 \\ & 65 \\ & 82 \end{aligned}$ | W |
| Diode Power Dissipation per die ( $\mathrm{T}^{\mathrm{C}}=25^{\circ} \mathrm{C}$ ) | 5A120 10A120 15A120 | PD | $\begin{aligned} & 19 \\ & 38 \\ & 38 \end{aligned}$ | W |
| IGBT Power Dissipation per die ( ${ }^{\text {C }} \mathrm{C}=95^{\circ} \mathrm{C}$ ) | 5A120 10A120 15A120 | PD | $\begin{aligned} & \hline 19 \\ & 29 \\ & 36 \end{aligned}$ | W |
| Diode Power Dissipation per die ( $\left.\mathrm{T}_{\mathrm{C}}=95^{\circ} \mathrm{C}\right)$ | 5A120 10A120 15A120 | PD | $\begin{aligned} & \hline 8.3 \\ & 17 \\ & 17 \end{aligned}$ | W |
| Junction Temperature Range |  | TJ | -40 to +150 | ${ }^{\circ} \mathrm{C}$ |
| Short Circuit Duration ( $\left.\mathrm{V}_{\mathrm{CC}}=600 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=125^{\circ} \mathrm{C}\right)$ |  | $\mathrm{t}_{\text {sc }}$ | 10 | $\mu \mathrm{sec}$ |

(1) $1.0 \mathrm{~ms}=1.0 \%$ duty cycle

This document contains information on a product under development. Motorola reserves the right to change or discontinue this product without notice.
Preferred devices are Motorola recommended choices for future use and best overall value.
REV 2
MOTOROLA

MAXIMUM DEVICE RATINGS $\left(T_{J}=25^{\circ} \mathrm{C}\right.$ unless otherwise noted) - continued

| Rating | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Isolation Voltage | $\mathrm{V}_{\text {ISO }}$ | 2500 | V |
| Operating Case Temperature Range | $\mathrm{T}^{\mathrm{C}}$ | -40 to +95 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature Range | $\mathrm{T}_{\text {stg }}$ | -40 to +125 | ${ }^{\circ} \mathrm{C}$ |
| Mounting Torque - Heat Sink Mounting Holes (\#8 or M4 screws) | - | 12 | $\mathrm{Ib}^{\mathrm{lb}-\mathrm{in}}$ |

ELECTRICAL CHARACTERISTICS $\left(T_{J}=25^{\circ} \mathrm{C}\right.$ unless otherwise noted)

| Characteristic | Symbol | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Gate-Emitter Leakage Current ( $\left.\mathrm{V}_{\mathrm{CE}}=0 \mathrm{~V}, \mathrm{~V}_{\mathrm{GE}}= \pm 20 \mathrm{~V}\right)$ | IGES | - | - | $\pm 20$ | $\mu \mathrm{A}$ |
| $\begin{aligned} & \text { Collector-Emitter Leakage Current }\left(\mathrm{V}_{\mathrm{CE}}=1200 \mathrm{~V}, \mathrm{~V}_{\mathrm{GE}}=0 \mathrm{~V}\right) \\ & \mathrm{T}_{\mathrm{J}}=125^{\circ} \mathrm{C} \end{aligned}$ | ICES | - | $\begin{gathered} \hline 6.0 \\ 2000 \end{gathered}$ | 100 | $\mu \mathrm{A}$ |
| Gate-Emitter Threshold Voltage ( $\left.\mathrm{V}_{\mathrm{CE}}=\mathrm{V}_{\mathrm{GE}}, \mathrm{I}_{\mathrm{C}}=1.0 \mathrm{~mA}\right)$ | $\mathrm{V}_{\mathrm{GE}}$ (th) | 4.0 | 6.0 | 8.0 | V |
| Collector-Emitter Breakdown Voltage ( $\mathrm{I} \mathrm{C}=10 \mathrm{~mA}, \mathrm{~V}_{\mathrm{GE}}=0 \mathrm{~V}$ ) | $\mathrm{V}_{\text {(BR) }}$ CES | 1200 | - | - | V |
| $\begin{aligned} & \text { Collector-Emitter Saturation Voltage ( } \mathrm{I} \mathrm{C}=\mathrm{I}_{\mathrm{Cmax}}, \mathrm{~V}_{\mathrm{GE}}=15 \mathrm{~V} \text { ) } \\ & \mathrm{T}_{\mathrm{J}}=125^{\circ} \mathrm{C} \end{aligned}$ | $V_{\text {CE }}$ (SAT) |  | $\begin{aligned} & 2.54 \\ & 2.33 \end{aligned}$ | 3.5 | V |
| $\begin{aligned} & \text { Diode Forward Voltage }\left(\mathrm{I}_{\mathrm{F}}=\mathrm{I}_{\mathrm{Fmax}}, \mathrm{~V}_{\mathrm{GE}}=0 \mathrm{~V}\right) \\ & \mathrm{T}_{\mathrm{J}}=125^{\circ} \mathrm{C} \end{aligned}$ | $\mathrm{V}_{\mathrm{F}}$ | $-$ | $\begin{aligned} & 1.67 \\ & 1.31 \end{aligned}$ | 2.0 | V |
| $\begin{array}{ll}\left.\text { Input Capacitance ( } \mathrm{V}_{\mathrm{CE}}=10 \mathrm{~V}, \mathrm{~V}_{\mathrm{GE}}=0 \mathrm{~V}, \mathrm{f}=1.0 \mathrm{Mhz}\right) & 5 \mathrm{~A} 120 \\ & 10 \mathrm{~A} 120 \\ & 15 \mathrm{~A} 120\end{array}$ | Cies | - | $\begin{gathered} \hline 930 \\ 1200 \\ 2800 \end{gathered}$ | - | pF |
| $\begin{array}{ll}\text { Input Gate Charge }\left(\mathrm{V}_{\mathrm{CE}}=600 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=\mathrm{I}_{\mathrm{Cmax}}, \mathrm{V}_{\mathrm{GE}}=15 \mathrm{~V}\right) & 5 \mathrm{~A} 120 \\ & 10 \mathrm{~A} 120 \\ & 15 \mathrm{~A} 120\end{array}$ | QT | - | $\begin{gathered} 31 \\ 65 \\ 100 \end{gathered}$ | - | nC |

INDUCTIVE SWITCHING CHARACTERISTICS $\left(T_{J}=25^{\circ} \mathrm{C}\right)$

| Recommended Gate Resistor  <br> Turn-On 5A120 <br>  10 A 120 <br> Turn-Off 15 A 120 | $\mathrm{R}_{\mathrm{G}(\mathrm{on})}$ <br> $\mathrm{R}_{\mathrm{G} \text { (off) }}$ | - | $\begin{gathered} 270 \\ 220 \\ 220 \\ 20 \end{gathered}$ | - | $\Omega$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{array}{\|ll} \hline \text { Turn-On Delay Time } & \\ \left(\mathrm{V}_{\mathrm{CE}}=600 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=\mathrm{I}_{\mathrm{Cmax}}, \mathrm{~V}_{\mathrm{GE}}=15 \mathrm{~V}, \mathrm{R}_{\mathrm{G}}\right. \text { as specified) } & \\ & 5 \mathrm{~A} 120 \\ & 10 \mathrm{~A} 120 \\ & 15 \mathrm{~A} 120 \end{array}$ | $\mathrm{t}_{\mathrm{d}(\mathrm{on})}$ | - | $\begin{aligned} & 255 \\ & 350 \\ & 425 \end{aligned}$ | - | ns |
| $\begin{array}{ll} \hline \text { Rise Time } \\ \quad\left(\mathrm{V}_{\mathrm{CE}}=600 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=\mathrm{I}_{\mathrm{Cmax}}, \mathrm{~V}_{\mathrm{GE}}=15 \mathrm{~V}, \mathrm{R}_{\mathrm{G}}\right. \text { as specified) } & \\ & 5 \mathrm{~A} 120 \\ & 10 \mathrm{~A} 120 \\ & 15 \mathrm{~A} 120 \end{array}$ | $\mathrm{tr}_{r}$ | - | $\begin{aligned} & 140 \\ & 250 \\ & 225 \end{aligned}$ | - | ns |
| Turn-Off Delay Time <br> $\left(\mathrm{V}_{\mathrm{CE}}=600 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=\mathrm{I}_{\mathrm{Cmax}}, \mathrm{V}_{\mathrm{GE}}=15 \mathrm{~V}, \mathrm{R}_{\mathrm{G}}\right.$ as specified $)$ | ${ }^{\text {d }}$ (off) | - | 170 | - | ns |
| Fall Time ( $\mathrm{V}_{\mathrm{CE}}=600 \mathrm{~V}$, $\mathrm{I}_{\mathrm{C}}=\mathrm{I}_{\text {Cmax }}, \mathrm{V}_{\mathrm{GE}}=15 \mathrm{~V}, \mathrm{R}_{\mathrm{G}}$ as specified) | $t_{f}$ | - | 290 | 500 | ns |
| Turn-On Energy $\left(\mathrm{V}_{\mathrm{CE}}=600 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=\mathrm{I}_{\mathrm{Cmax}}, \mathrm{~V}_{\mathrm{GE}}=15 \mathrm{~V}, \mathrm{R}_{\mathrm{G}} \text { as specified }\right) ~ \begin{array}{ll}  & \\ & \text { AA120 } \\ & 10 \mathrm{~A} 120 \\ & 15 \mathrm{~A} 120 \end{array}$ | $\mathrm{E}_{(0 n)}$ | - | $\begin{gathered} 0.96 \\ 2.8 \\ 4.0 \end{gathered}$ | - | mJ |
| $\begin{array}{\|ll} \hline \text { Turn-Off Energy } \\ \left(\mathrm{V}_{\mathrm{CE}}=600 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=\mathrm{I}_{\mathrm{Cmax}}, \mathrm{~V}_{\mathrm{GE}}=15 \mathrm{~V}, \mathrm{R}_{\mathrm{G}}\right. \text { as specified) } & \\ & 5 \mathrm{~A} 120 \\ & 10 \mathrm{~A} 120 \\ & 15 \mathrm{~A} 120 \end{array}$ | $\mathrm{E}_{\text {(off) }}$ | - | $\begin{aligned} & 0.15 \\ & 0.39 \\ & 0.52 \end{aligned}$ | $\begin{aligned} & 1.0 \\ & 2.0 \\ & 2.5 \end{aligned}$ | mJ |
| Diode Reverse Recovery Time | trr | - | $\begin{aligned} & 130 \\ & 170 \\ & 165 \end{aligned}$ | - | ns |


| Characteristic |  | Symbol | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| INDUCTIVE SWITCHING CHARACTERISTICS $\left(\mathrm{T}_{J}=25^{\circ} \mathrm{C}\right)$ - continued |  |  |  |  |  |  |
| Peak Reverse Recovery Current ( $\mathrm{I} \mathrm{F}=\mathrm{I}_{\mathrm{Fmax}}, \mathrm{V}=600 \mathrm{~V}, \mathrm{R}_{\mathrm{G}}$ as specified) | $\begin{aligned} & \text { 5A120 } \\ & \text { 10A120 } \\ & \text { 15A120 } \end{aligned}$ | Irrm | - | $\begin{aligned} & 5.0 \\ & 6.0 \\ & 9.6 \end{aligned}$ | - | A |
| Diode Stored Charge <br> ( $\mathrm{I}_{\mathrm{F}}=\mathrm{I}_{\mathrm{Fmax}}, \mathrm{V}=600 \mathrm{~V}, \mathrm{R}_{\mathrm{G}}$ as specified) | 5A120 <br> 10A120 <br> 15A120 | Qrr | - | $\begin{aligned} & 335 \\ & 575 \\ & 860 \end{aligned}$ | - | $n \mathrm{C}$ |

INDUCTIVE SWITCHING CHARACTERISTICS $\left(T_{J}=125^{\circ} \mathrm{C}\right)$

| Characteristic | Symbol | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{array}{r} \text { Turn-On Delay Time } \\ \left(\mathrm{V}_{\mathrm{CE}}=600 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=\mathrm{I}_{\mathrm{Cmax}}, \mathrm{~V}_{\mathrm{GE}}=15 \mathrm{~V}, \mathrm{R}_{\mathrm{G}}\right. \text { as specified) } \\ 5 \mathrm{~A} 120 \\ 10 \mathrm{~A} 120 \\ 15 \mathrm{~A} 120 \end{array}$ | ${ }_{\text {t }}(\mathrm{on})$ | - | $\begin{aligned} & 230 \\ & 315 \\ & 375 \end{aligned}$ | - | ns |
| $\begin{array}{\|l} \hline \begin{array}{l} \text { Rise Time } \\ \left(V_{C E}=600 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=\mathrm{I}_{\mathrm{Cmax}}, \mathrm{~V}_{\mathrm{GE}}=15 \mathrm{~V}, \mathrm{R}_{\mathrm{G}}\right. \text { as specified) } \\ 5 \mathrm{~A} 120 \\ 10 \mathrm{~A} 120 \\ 15 \mathrm{~A} 120 \end{array} \end{array}$ | $\mathrm{tr}_{r}$ |  | $\begin{aligned} & 130 \\ & 220 \\ & 235 \end{aligned}$ | - | ns |
| Turn-Off Delay Time $\left(\mathrm{V}_{\mathrm{CE}}=600 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=\mathrm{I}_{\mathrm{Cmax}}, \mathrm{V}_{\mathrm{GE}}=15 \mathrm{~V}, \mathrm{R}_{\mathrm{G}}\right.$ as specified $)$ | $\mathrm{t}_{\mathrm{d}}$ (off) | - | 176 | - | ns |
| Fall Time <br> $\left(\mathrm{V}_{\mathrm{CE}}=600 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=\mathrm{I}_{\mathrm{Cmax}}, \mathrm{V}_{\mathrm{GE}}=15 \mathrm{~V}, \mathrm{R}_{\mathrm{G}}\right.$ as specified $)$ | $t_{f}$ | - | 676 | - | ns |
| $\begin{array}{r} \text { Turn-On Energy } \\ \left(\mathrm{V}_{\mathrm{CE}}=600 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=\mathrm{I}_{\mathrm{Cmax}}, \mathrm{~V}_{\mathrm{GE}}=15 \mathrm{~V}, \mathrm{R}_{\mathrm{G}}\right. \text { as specified) } \\ 5 \mathrm{~A} 120 \\ 10 \mathrm{~A} 120 \\ 15 \mathrm{~A} 120 \end{array}$ | $\mathrm{E}_{\text {(on) }}$ | - | $\begin{aligned} & 1.3 \\ & 3.9 \\ & 5.5 \end{aligned}$ | - | mJ |
| ```Turn-Off Energy (VCE =600 V, IC = ICmax, VGE = 15 V, R 5A120 10A120 15A120``` | $\mathrm{E}_{\text {(off) }}$ | - | $\begin{aligned} & 0.711 \\ & 1.290 \\ & 1.939 \end{aligned}$ | - | mJ |
| Diode Reverse Recovery Time | trr | - | $\begin{aligned} & 190 \\ & 375 \\ & 310 \end{aligned}$ | - | ns |
| Peak Reverse Recovery Current | Irrm | - | $\begin{aligned} & 8.4 \\ & 10 \\ & 15 \end{aligned}$ | - | A |
| Diode Stored Charge | Qrr | - | $\begin{gathered} 825 \\ 2100 \\ 2500 \end{gathered}$ | - | nC |

THERMAL CHARACTERISTICS (Each Die)

| Thermal Resistance - IGBT | 5A120 | $\mathrm{R}_{\theta \mathrm{JC}}$ | - | 2.30 | 2.88 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| :--- | :--- | :---: | :---: | :---: | :---: | :---: |
|  | 10A120 |  | - | 1.54 | 1.92 |  |
|  | 15A120 |  | - | 1.21 | 1.52 |  |
| Thermal Resistance - Free-Wheeling Diode | 5A120 | R $_{\theta \mathrm{JCC}}$ | - | 5.28 | 6.60 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
|  | 10A120 |  | - | 2.61 | 3.26 |  |
|  | 15A120 |  | - | 2.61 | 3.26 |  |



Figure 1. Normalized $\mathrm{I}_{\mathrm{C}}$ versus $\mathrm{V}_{\mathrm{CE}}, \mathrm{T}_{\mathrm{J}}=\mathbf{2 5}{ }^{\circ} \mathrm{C}$


Figure 3. $I_{F}$ versus $V_{F}$


Figure 5. $\mathbf{t}_{\mathrm{d}}$ (off), $\mathbf{t}_{\mathbf{f}}, \mathrm{t}_{\mathbf{o f f}}$, versus $\mathrm{R}_{\mathbf{G}}$


Figure 2. Normalized $\mathrm{I}_{\mathrm{C}}$ versus $\mathrm{V}_{\mathrm{CE}}, \mathrm{T}_{\mathbf{J}}=125^{\circ} \mathrm{C}$


Figure 4. $\mathrm{t}_{\mathrm{d}(\mathrm{off})}, \mathrm{t}_{\mathrm{f}}, \mathrm{t}_{\text {off }}$ versus Normalized $\mathrm{I}_{\mathrm{C}}$


Figure 6. $\mathrm{t}_{\mathrm{d}}(\mathrm{on}), \mathrm{t}_{\mathrm{r}}$, $\mathrm{t}_{\mathrm{on}}$ versus $\mathrm{I}_{\mathrm{C}}$

TYPICAL CHARACTERISTICS


Figure 7. $\mathbf{t}_{\mathbf{d}(0 n)}, \mathbf{t}_{\mathbf{r}}$, $\mathrm{t}_{\mathbf{o n}}$ versus Normalized $\mathbf{R}_{\mathbf{G}}$


Figure 8. Eon, Eoff versus IC


Figure 10. Normalized Eon versus Normalized $\mathrm{R}_{\mathrm{G}(\mathrm{on})}$


Figure 12. Capacitance Variation


Figure 13. $\mathbf{V}_{\mathbf{G E}}$ versus $\mathbf{Q}_{\mathbf{G}}$


Figure 14. Reverse Biased Safe Operating Area


Figure 15. Normalized $r(t)$


Figure 16. Switching Waveforms


Figure 17. Typical Gate Drive Circuit

## APPLICATION INFORMATION

These modules are designed to be used as the power stage of a three-phase AC induction motor drive. They may be used for up to 230 VAC applications. Switching frequencies up to 10 kHz have been considered in the design.

Gate resistance recommendations have been listed. Separate turn-on and turn-off resistors are listed, to be used in a circuit resembling Figure 17. All switching characteristics are given based on following these recommendations, but appropriate graphs are shown for operation with different gate resistance. In order to equalize across the three different module ratings, a normalization process was used. Actual typical values are listed in the second section of this specification sheet, "Electrical Specifications," but many of the graphs are given in normalized units.

The first three graphs, the DC characteristics, are normalized for current. The devices are designed to operate the same at rated maximum current (10 and 20 A). The curves extend to $I_{\text {Cpk, }}$ the maximum allowable instantaneous current.

The next graph, turn-off times versus current, is again normalized to the rated maximum current. The following graph, turn-off times versus $\mathrm{R}_{\mathrm{G}}$ (off), is intentionally not normalized, as all three modules behave similarly during turn-off.

Turn-on times have been normalized. Again, the graph showing variation due to current has been normalized for rated maximum current. The graph showing variation due to gate resistance normalizes against the recommended $\mathrm{R}_{\mathrm{G}}$ (on) for each module. In addition, the times are normalized to $t_{r}$ at the appropriate temperature. For example, $\mathrm{t}_{\mathrm{d}}(\mathrm{on})$ for a 10 A module operating at $125^{\circ} \mathrm{C}$ at 4.0 A can be found by multiplying the typical $\mathrm{tr}_{\mathrm{r}}$ for a 10 A module at $125^{\circ} \mathrm{C}(220 \mathrm{~ns})$ by the value shown on the graph at a normalized current of 0.4 (1.4) to get 308 ns . The most salient features demonstrated by these graphs are the general trends: rise time is a
larger fraction of total turn-on time at $125^{\circ} \mathrm{C}$, and in general, larger gate resistance results in slower switching.

Graphs of switching energies follow a similar structure. The first of these graphs, showing variation due to current, is not normalized, as any of these devices operating within its limits follows the same trend. $\mathrm{E}_{\text {off }}$ does not need to be normalized to show variation with $\mathrm{R}_{\mathrm{G}}$ (off), as all three are specified with the same nominal resistance. $E_{o n}$, however, has been appropriately normalized. Gate resistance has been normalized to the specified $\mathrm{R}_{\mathrm{G}}(\mathrm{on})$. In order to show the effect of elevated temperature, all energies were normalized to $\mathrm{E}_{\text {on }}$ at $25^{\circ} \mathrm{C}$ using the recommended $\mathrm{R}_{\mathrm{G}}$ (on).

Reverse recovery characteristics are also normalized. IF is normalized to rated maximum current. Irrm is normalized so that at maximum current at either $25^{\circ} \mathrm{C}$ or $125^{\circ} \mathrm{C}$, the graph indicates " 10 ", while $t_{r r}$ is normalized to be " 1 " at maximum current at either temperature.

Capacitance values are normalized for ICmax. Due to poor scaling, gate charge and thermal characteristics are shown separately for each module.

Many issues must be considered when doing PCB layout. Figure 19 shows the footprint of a module, allowing for reasonable tolerances. A polarizing post is provided near pin 1 to ensure that the module is properly inserted during final assembly. When laying out traces, two issues are of primary importance: current carrying capacity and voltage clearance. Many techniques may be used to maximize both, including using traces on both sides of the PCB to double total copper thickness, providing cut-outs in high-current traces near high-voltage pins, and even removing portions of the board to increase "over-the-surface" creapage distance. Some additional advantage may be gained by potting the entire board assembly in a good dielectric. Consult appropriate regulatory standards, such as UL 840, for more details on high-voltage creapage and clearance.


Figure 18. Schematic of Internal Circuit, Showing Package Pin-Out


Figure 19. Package Footprint

NOTES:

1. Package is symmetrical, except for a polarizing plastic post near pin 1 , indicated by a non-plated thru-hole in the footprint.
2. Dimension of plated thru-holes indicates net size after plating.
3. Access holes for mounting screws may or may not be necessary depending on assembly plan for finished product.

## PACKAGE DIMENSIONS



PRELIMINARY

Motorola reserves the right to make changes without further notice to any products herein. Motorola makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Motorola assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters which may be provided in Motorola data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. Motorola does not convey any license under its patent rights nor the rights of others. Motorola products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Motorola product could create a situation where personal injury or death may occur. Should Buyer purchase or use Motorola products for any such unintended or unauthorized application, Buyer shall indemnify and hold Motorola and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Motorola was negligent regarding the design or manufacture of the part. Motorola and $\mathbb{M}$ ) are registered trademarks of Motorola, Inc. Motorola, Inc. is an Equal Opportunity/Affirmative Action Employer.

Mfax is a trademark of Motorola, Inc.

## How to reach us:

USA/EUROPE/ Locations Not Listed: Motorola Literature Distribution; P.O. Box 5405, Denver, Colorado 80217. 303-675-2140 or 1-800-441-2447

Mfax ${ }^{\text {TM }: ~ R M F A X 0 @ e m a i l . s p s . m o t . c o m ~-~ T O U C H T O N E ~ 602-244-6609 ~}$ - US \& Canada ONLY 1-800-774-1848

JAPAN: Nippon Motorola Ltd.: SPD, Strategic Planning Office, 4-32-1, Nishi-Gotanda, Shinagawa-ku, Tokyo 141, Japan. 81-3-5487-8488

ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852-26629298
INTERNET: http://motorola.com/sps

